WHAT IS CLAIMED IS:

A power supply circuit, comprising:

a first circuit for generating a positive polarity voltage;

a first terminal for outputting the positive polarity voltage from said first circuit;

a second circuit for generating a negative polarity voltage;

a second terminal for outputting the negative polarity voltage from said second circuit; and

a short circuit for short-circuiting substantially between said first terminal and said second terminal in response to a power-off signal.

2. A power supply circuit according to claim 1, wherein said short-circuit includes a series circuit having a switching element and a current-limiting element connected between said first terminal and said second terminal.

3. A power supply circuit according to claim 1, wherein said short circuit includes a switching element connected between said first terminal and said second terminal.

4. A power supply circuit according to claim 1, wherein said first circuit includes a chopper circuit for generating a low first positive voltage, and a fly-back circuit for receiving the first positive voltage from the chopper circuit to generate a high second positive voltage, and further comprising:

first and second output terminals for respectively outputting the first positive voltage and the second positive voltage; and

a diode connected between said first and second output terminals in a forward direction of from said first positive voltage output terminal to said second positive voltage output terminal.

5. A power supply circuit, comprising:

a chopper circuit for generating a low first voltage;

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a fly-back circuit for receiving the first voltage from said chopper circuit to generate a high second voltage;

first and second terminals for respectively outputting the first and second voltages;

a diode connected between said first terminal and said second terminal in a forward direction of from said first terminal to said second terminal.

6. A camera, comprising:

a first circuit for generating a positive polarity voltage;

a first terminal for outputting the positive polarity voltage from said first circuit;

a second circuit for generating a negative polarity voltage;

a second terminal for outputting the negative polarity voltage from said second circuit;

a short circuit for short-circuiting substantially between said first terminal and said second terminal in response to a power-off signal; and

a CCD imager for receiving the positive polarity voltage and negative polarity voltage through said first terminal and said second terminal.

A camera, comprising:

a chapper circuit for generating a low first voltage;

a fly-back circuit for receiving the first voltage from said chopper circuit to

20 generate a high second voltage;

first and second terminals for respectively outputting the first and second voltages;

a diode connected between said first terminal and said second terminal in a

forward direction of from said first terminal to said second terminal; and

a CCD imager for receiving the positive polarity voltage and negative polarity voltage through said first terminal and said second terminal.

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